

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANTS: HSU et al.

EXAMINER: TRAN, Andrew Q.

SERIAL NO: 10/673,801

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ON: SEGMENTED CONTENT ADDRESSABLE MEMORY ARCHITECTURE
FOR IMPROVED CYCLE TIME AND REDUCED POWER CONSUMPTION

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL AMENDMENT

This paper is responsive to the Examiner's Interview Summary mailed on July 5, 2007. The Examiner is requested to enter and consider the following amendments and remarks.